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| Yung Chang Hu | EMERGING DISPLAY | ISSUE : OCT.24, 2007 |
| PROVED BY: | TECHNOLOGIES CORPORATION | TOTAL PAGE: 22 |
| David Chang | | VERSION: 1 |
| CUSTOMER | ACCEPTANCE SPEC | CIFICATIONS |
| CUSTOMER'S APPROV | DEL NO.: ET057011DM6 (RoHS) MESSRS: | |
| DATE : | | |
| BY: | | |
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| EMERG | | | MODEL NO. | VERSION | PAGE |
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| TECHNOL | OGIES CORP | ORATION | ET057011DM6 | 1 | 0-1 |
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- 1. GENERAL SPECIFICATIONS
 - 1.1 APPLICATION NOTES FOR CONTROLLER/DRIVER PLEASE REFER TO :

HIMAX HX8218 HIMAX HX8615

1.2 MATERIAL SAFETY DESCRIPTION

ASSEMBLIES SHALL COMPLY WITH EUROPEAN ROHS REQUIREMENTS, INCLUDING PROHIBITED MATERIALS/COMPONENTS CONTAINING LEAD, MERCURY, CADMIUM, HEXAVALENT CHROMIUM, POLYBROMINATED BIPHENYLS (PBB) AND POLYBROMINATED DIPHENYL ETHERS (PBDE)

| 2. | MECHANICAL | SPECIFICATIONS |
|----|------------|-----------------------|
|----|------------|-----------------------|

| WEETH HATCHE STEEN TEITHOUG | |
|-----------------------------|----------------------------|
| (1) DISPLAY SIZE (inch) | 5.7" |
| (2) NUMBER OF DOTS | 320W * (RGB) * 240H DOTS |
| (3) MODULE SIZE | 124.7W * 100.0H * 10.5D mm |
| | (WITHOUT FPC) |
| (4) EFFECTIVE AREA | 117.2W * 88.4H mm |
| (5) ACTIVE AREA | 115.2W * 86.4H mm (LCD) |
| (6) DOT SIZE | 0.12W * 0.36H mm |
| (7) PIXEL SIZE | 0.36W * 0.36H mm |
| (8) LCD TYPE | TFT, TRANSMISSIVE |
| (9) COLOR | 16.7M (24BIT) |
| (10) VIEWING DIRECTION | 6 O'CLOCK |
| (11) BACK LIGHT | LED . COLOR : WHITE |

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3. ABSOLUTE MAXIMUM RATINGS

3.1 ELECTRICAL ABSOLUTE MAXIMUM RATINGS.

| PARAMETER | SYMBOL | MIN. | MAX. | UNIT | REMARK |
|--------------------------|--------|------|---------|------|--------|
| POWER SUPPLY VOLTAGE | VDD | -0.3 | 7.0 | V | VSS=0 |
| FOWER SUFFLY VOLTAGE | VCC | -0.3 | 7.0 | V | |
| INPUT SIGNAL VOLTAGE | VL | -0.3 | VCC+0.3 | V | |
| LED BACKLIGH DISSIPATION | PD | | 1.28 | W | |
| LED BACKLIGHT CURRENT | IF | | 0.06 | A | |
| LED BACKLIGHT REVERSE | VR | | 45 | V | |
| VOLTAGE | V IX | | 40 | • | |

3.2 ENVIRONMENTAL ABSOLUTE MAXIMUM RATINGS.

| ITEM | OPERATING | | STORAGE | | REMARK | |
|---------------------|-----------|------------------------------------|----------------|-----------------------------------|--|--|
| I I E WI | MIN. | MAX. | MIN. | MAX. | REMARK | |
| AMBIENT TEMPERATURE | -20°C | 70°C | -30°C | 80°C | NOTE(2),(3) | |
| HUMIDITY | NOTI | E (1) | NOT | E(4) | WITHOUT | |
| HUMIDIT I | NOTI | C(4) | NOT | C(4) | CONDENSATION | |
| VIBRATION | _ | 2.45 m/s ² (0.25 G) | _ | 11.76 m/s ² (1.2 G) | 5~20Hz , 1HR 20~500Hz(20Hz) , 1HR 20~500Hz(500Hz) , 1HR X,Y,Z,TOTAL 3HR | |
| SHOCK | _ | 29.4 m/s ² (3 G) | _ | 490 m/s ² (5 0 G) | 10 m SECONDS XYZ DIRECTIONS 1 TIME EACH | |
| CORROSIVE GAS | NOT ACC | EPTABLE | NOT ACCEPTABLE | | | |

NOTE (2): Ta AT -30°C: 48HR MAX.

80°C: 168HR MAX.

NOTE (3): BACKGROUND COLOR CHANGES SLIGHTLY DEPENDING ON AMBIENT

TEMPERATURE THIS PHENOMENON IS REVERSIBLE.

NOTE (4) : $Ta \le 60$ °C: 90%RH MAX (96HRS MAX).

 $\text{Ta} > 60 ^{\circ}\text{C} : AB\text{SOLUTE}$ HUMIDITY MUST BE LOWER THAN THE HUMIDITY

OF 90%RH AT 60°C(96HRS MAX).

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4. ELECTRICAL CHARACTERISTICS

Ta = 25 °C

| PARAMETER | SYMBOL | CONDITION | MIN. | TYP. | MAX. | UNIT | REMARK |
|-------------------------------|-------------|--------------------------|---------|-------|---------|------|---------|
| POWER SUPPLY VOLTAGE | VDD | _ | 3 | 3.3 | 3.6 | V | |
| FOR LCD | | | | | | | |
| POWER SUPPLY VOLTAGE | VCC | _ | 3 | 3.3 | 3.6 | V | |
| FOR VCOM | | | | | | | |
| POWER SUPPLY CURRENT FOR LCD | IDD | VCC=3.3V | _ | (8) | (11) | mA | |
| POWER SUPPLY CURRENT FOR VCOM | ICC | VDD = 3.3V LED B/L=ON | _ | (510) | (600) | mA | |
| LOW LEVEL INPUT VOLTAGE | VIL | _ | 0 | _ | 0.3*VCC | V | |
| HIGH LEVEL INPUT VOLTAGE | VIH | | 0.7*VCC | _ | VCC | V | |
| LOW LEVEL OUTPUT VOLTAGE | VOL | $IOL = 400 \mu A$ | 0 | _ | 0.2*VCC | V | |
| HIGH LEVEL OUTPUT VOLTAGE | VOH | $IOH = -400 \mu A$ | 0.8*VCC | _ | VCC | V | |
| FRAME FREQUENCY | fFRAME | | _ | 60 | 90 | Hz | |
| DOT DATA CLOCK | DCLK | | _ | 6.4 | | MHz | |
| FORWARD VOLTAGE | $V_{\rm F}$ | I _F =40mA | 28 | 30 | 32 | V | NOTE(1) |
| LED LIFE TIME | | _ | 30000 | 40000 | | hr | |

NOTE (1): INTERNAL CIRCUIT DIAGRAM OF BACKLIGHT

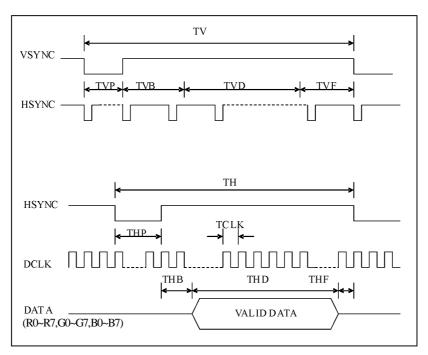


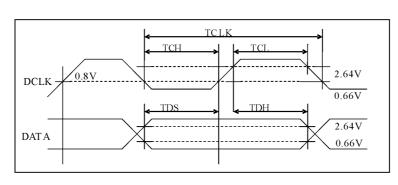
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5. TIMING CHART

5.1 DIGITAL PARALLEL RGB INTERFACE

| SIGNAL | ITEM | | SYMBOL | MIN. | TYP. | MAX. | UNIT |
|--------|----------------|-------------|--------|------|-------------|------|------|
| | FREQUENCY | TCLK | | 6.4 | _ | MHz | |
| DCLK | HIGH TIME | | TCH | | 78 | _ | ns |
| | LOW TIME | | TCL | | 78 | _ | ns |
| DATA | SETUP TIME | | TDS | 12 | | _ | ns |
| DATA | HOLD TIME | | TDH | 12 | | _ | ns |
| | PERIOD | | TH | | 408 | _ | DCLK |
| | PULSE WIDTH | | THP | | 30 | _ | DCLK |
| HSYNC | BACK-PORCH | | THB | _ | 38 | _ | DCLK |
| | DISPLAY PERIOD | | THD | _ | 320 | _ | DCLK |
| | FRONT-PORCH | | THF | _ | 20 | _ | DCLK |
| | PERIOD | NTSC | TV | | 262.5 | | TH |
| | FERIOD | PAL | | _ | 312.5 | | 111 |
| | PULSE WIDTH | | TVP | 1 | 3 | 5 | TH |
| VSYNC | BACK-PORCH | NTSC | TVB | | 15 | | TH |
| VSTNC | BACK-PORCH PAL | | TVD | | 23 | | 111 |
| | DISPLAY PERIOD | | TVD | _ | 240 | _ | TH |
| | FRONT-PORCH | NTSC PAL | TVF | _ | 4.5 46.5 | _ | TH |





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6. OPTICAL CHARACTERISTICS (NOTE 1)

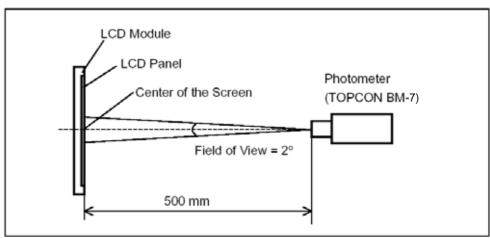
6.1 OPTICAL CHARACTERISTICS

 $Ta = 25 \pm 2$ °C

| | | | | | | | | · | | |
|--------------------------|-------|---------------------|--|----------------------------|-------------|------|------|-------------------|--------|--|
| ITE | M | SYMBOL | COND | ITION | MIN. | TYP. | MAX. | UNIT | REMARK | |
| | | θ_{y^+} | | $\theta_{\rm x}=0^{\circ}$ | 55 | 60 | | | | |
| VIEWING ANGLE | | $\Theta_{	ext{y-}}$ | CR ≥ 10 | O _X -O | 70 | 75 | | 4 | (2) | |
| | | θ_{x^+} | CR ≥ 10 | θ _v =0° | 70 | 75 | | deg. | (3) | |
| | | θ_{x} | | θ_{y} –0 | 70 | 75 | | | | |
| CONTRAST RAT | Oľ | CR | θx=0°, | θу=0° | 300 | 400 | | _ | (3) | |
| RESPONSE TIM | E | tr(rise) | 000 | 000 | | 15 | 30 | msaa | (2) | |
| RESPONSE TIME | | t f (fall) | $\theta x=0^{\circ}, \ \theta y=0^{\circ}$ | | | 35 | 50 | msec | (2) | |
| THE BRIGHTNESS OF MODULE | | В | θx=0°, θy=0° If=40mA | | 450 | 500 | _ | cd/m ₂ | _ | |
| | WHITE | X | | | 0.27 | 0.32 | 0.37 | | (4) | |
| | | у | θx=0°, θ ₂ If=40m | | 0.30 | 0.35 | 0.40 | | | |
| COLOR OF | RED | X | | | 0.58 | 0.63 | 0.68 | | _ | |
| COLOR OF CIE | | у | | | 0.31 | 0.36 | 0.41 | | | |
| COORDINATE | GREEN | X | NTSC | | 0.28 | 0.33 | 0.38 | | | |
| COOKDINATE | OKEEN | у | | | 0.55 | 0.60 | 0.65 | | | |
| | BLUE | X | | | 0.09 | 0.14 | 0.19 | | | |
| | BLUE | у | | | 0.06 | 0.11 | 0.16 | | | |
| THE UNIFORMITY OF MODULE | | | _ | _ | 75 | 80 | | % | | |

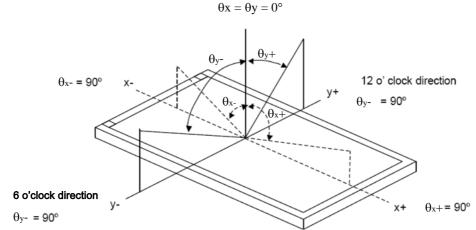
NOTE (1): TEST EQUIPMENT SETUP:

AFTER STABILIZING AND LEAVING THE PANEL ALONE AT A GIVEN TEMPERATURE FOR 30 MINUTES , THE MEASUREMENT SHOULD BE EXECUTED. MEASUREMENT SHOULD BE EXECUTED IN A STABLE , WINDLESS , AND DARK ROOM. OPTICAL SPECIFICATIONS ARE MEASURED BY TOPCON BM-7(FAST) WITH A VIEWING ANGLE OF 2° AT A DISTANCE OF 50cm AND NORMAL DIRECTION.



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NOTE (2): DEFINITION OF VIEWING ANGLE:

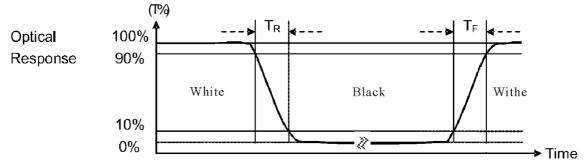


Normal

NOTE (3): DEFINITION OF CONTRAST RATIO:

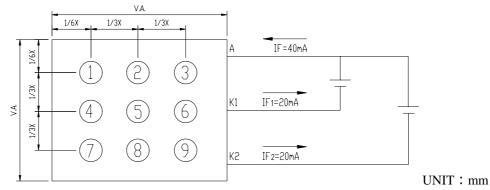
 $CONTRASTRATIO(CR) = \frac{BRIGHTNESS\ MEASURED\ WHEN\ LCD\ IS\ AT\ "WHITE\ STATE"}{BRIGHTNESS\ MEASURED\ WHEN\ LCD\ IS\ AT\ "BLACK\ STATE"}$

NOTE (4): DEFINITION OF RESPONSE TIME: TR AND TF
THE FIGURE BVELOW IS THE OUTPUT SIGNAL OF THE PHOTO DETECTOR.



NOTE (5): THE 100% TRANSMISSION IS DEFINED AS THE TRANSMISSION OF LCD PANEL WHEN ALL THE INPUT TERMINALS OF MODULE ARE ELECTRICALLY OPENED.

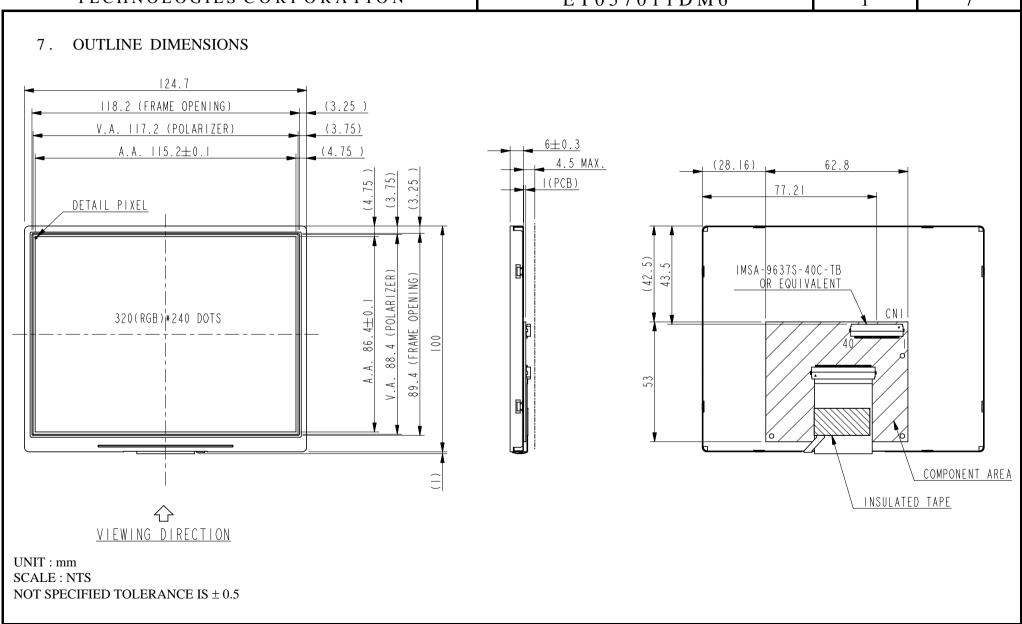
6.2 THE TEST METHOD OF BRIGHTNESS AND UNIFORMITY



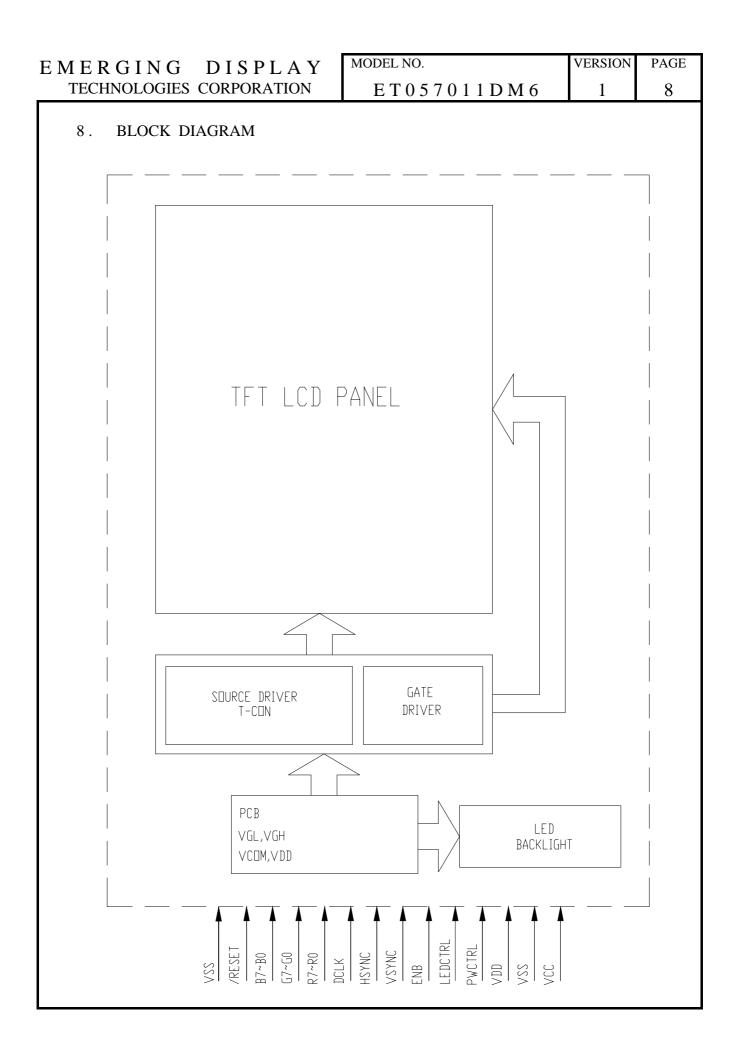
6.3 THE CALCULATING METHOD OF UNIFORMITY

UNIFORMITY: $\left[1 - \frac{\text{MAXIMUN BRIGHTESS-MINIMUN BRIGHTESS}}{\text{AVERAGE BRIGHTESS}}\right] \times 100\%$

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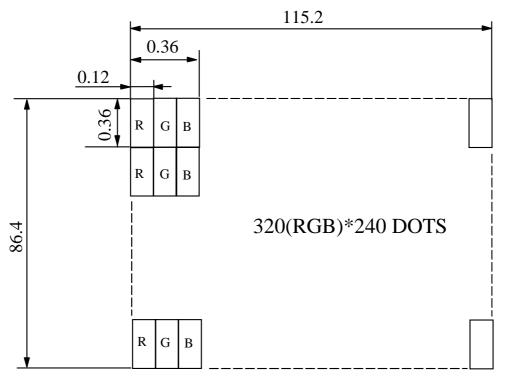
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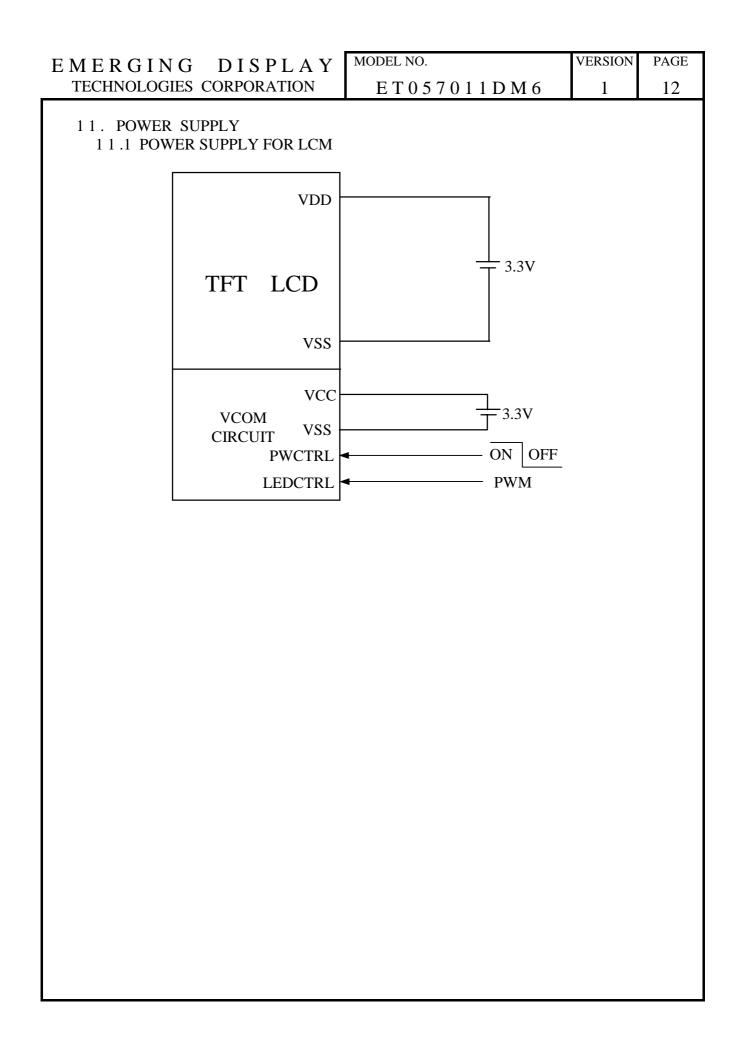


UNIT : mm SCALE : NTS

NOT SPECIFIED TOLERANCE IS \pm 0.1 DOTS MATRIX TOLERANCE IS \pm 0.01

| PIN NO | SYMBOL | I/O | FUNCTION |
|--------|---------|-----|--------------------------------|
| 1 | VSS | P | GROUND |
| 2 | VSS | P | GROUND |
| 3 | VSS | P | GROUND |
| 4 | VSS | P | GROUND |
| 5 | /RESET | I | HARDWARE RESET |
| 6 | В7 | I | BLUE DATA BIT 7 |
| 7 | В6 | I | BLUE DATA BIT 6 |
| 8 | B5 | I | BLUE DATA BIT 5 |
| 9 | B4 | I | BLUE DATA BIT 4 |
| 10 | В3 | I | BLUE DATA BIT 3 |
| 11 | B2 | I | BLUE DATA BIT 2 |
| 12 | B1 | I | BLUE DATA BIT 1 |
| 13 | В0 | I | BLUE DATA BIT 0 |
| 14 | G7 | I | GREEN DATA BIT 7 |
| 15 | G6 | I | GREEN DATA BIT 6 |
| 16 | G5 | I | GREEN DATA BIT 5 |
| 17 | G4 | I | GREEN DATA BIT 4 |
| 18 | G3 | I | GREEN DATA BIT 3 |
| 19 | G2 | I | GREEN DATA BIT 2 |
| 20 | G1 | I | GREEN DATA BIT 1 |
| 21 | G0 | I | GREEN DATA BIT 0 |
| 22 | R7 | I | READ DATA BIT 7 |
| 23 | R6 | I | READ DATA BIT 6 |
| 24 | R5 | I | READ DATA BIT 5 |
| 25 | R4 | I | READ DATA BIT 4 |
| 26 | R3 | I | READ DATA BIT 3 |
| 27 | R2 | I | READ DATA BIT 2 |
| 28 | R1 | I | READ DATA BIT 1 |
| 29 | R0 | I | READ DATA BIT 0 |
| 30 | DCLK | I | DOT DATA COLOCK |
| 31 | HSYNC | I | HORIZONTAL SYNC INPUT |
| 32 | VSYNC | I | VERTICAL SYNC INPUT |
| 33 | ENB | I | DATA ENABLE INPUT |
| 34 | LEDCRTL | I | BRIGHTNESS CONTROL FOR LED B/L |

| 10. IN | 10. INTERFACE SIGNALS | | | | | | | |
|--------|-----------------------|-----|--------------------------------------|--------------------------------------|--------------------|--|--|--|
| PIN NO | SYMBOL | I/O | | FUNCTION | | | | |
| 35 | PWCTRL | I | LOGIC LEVEL | PWCTRL H | REMARK POWER ON | | | |
| | | | H=3.3V L=0V | L | SHUTDOWN | | | |
| 36 | VDD | P | POWER SUPP | OWER SUPPLY FOR DIGITAL CIRCUIT | | | | |
| 37 | VSS | P | GROUND | | | | | |
| 38 | VCC | P | POWER SUPP | POWER SUPPLY FOR VCOM DRIVER CIRCUIT | | | | |
| 39 | VCC | P | POWER SUPPLY FOR VCOM DRIVER CIRCUIT | | | | | |
| 40 | VCC | P | POWER SUPP | LY FOR VCOM DRIVER C | RCUIT | | | |



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12. INSPECTION CRITERION

12.1 APPLICATION

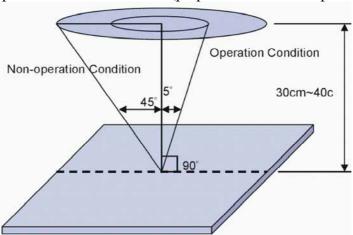
This inspection standard is to be applied to the LCD module delivered from EMERGING DISPLAY TECHNOLOGIES CORP.(E.D.T) to customers

12.2 INSPECTION CONDITIONS

12.2.1 (1)Observation Distance: 35cm±5cm

(2) View Angle:

Non-operation Condition : $\pm 5^{\circ}$ (perpendicular to LCD panel surface) Operation Condition : $\pm 45^{\circ}$ (perpendicular to LCD panel surface)



12.2.2 Environment Conditions:

| Amb | ient Temperature | 20°C~25°C | | |
|--------------|-----------------------|------------------|--|--|
| Am | bient Humidity | 65±20%RH | | |
| Ambient | Cosmetic Inspection | More than 600Lux | | |
| Illumination | Functional Inspection | 300~500 Lux | | |

12.2.3 Inspection lot

Quantity per delivery lot for each model

12.2.4 Inspection method

A sampling inspection shall be made according to the following provisions to judge The acceptability

(a) Applicable standard: MIL-STD-105E

Normal inspection, single sampling

Level

(b)AQL : Major defect : AQL 0.65% Minor defect : AQL 1.0%

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12.3 INSPECTION STANDARDS

12.3.1 VISUAL DEFECTS CLASSIFICATION

| TYPE OF DEFECT | INSPECTION ITEM | DEFECT FEATURE | AQL |
|----------------|--------------------------|---|------|
| | 1.DISPLAY ON | DEFECT TO MISS SPECIFIED DISPLAY FUNCTION, FOR ALL AND SPECIFIED DOTS EX: DISCONNECTION, SHORT CIRCUIT ETC | 0.65 |
| MAJOR DEFECT | 2.BACKLIGHT | NO LIGHT FLICKERING AND OTHER ABNORMAL ILLUMINATION | |
| | 3.DIMENSIONS | • SUBJECT TO INDIVIDUAL ACCEPTANCE SPECIFICATIONS | |
| | 1.DISPLAY ZONE | BLACK/WHITE SPOT BUBBLES ON POLARIZER NEWTON RING BLACK/WHITE LINE SCRATCH CONTAMINATION LEVER COLOR SPREED | |
| MINOR DEFECT | 2.BEZEL ZONE | STAINSSCRATCHESFOREIGN MATTER | 1.0 |
| | 3.SOLDERING | INSUFFICIENT SOLDER SOLDERED IN INCORRECT POSITION CONVEX SOLDERING SPOT SOLDER BALLS SOLDER SCRAPS | |
| | 4.DISPLAY ON (ALL ON) | • LIGHT LINE | |

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| | 12.3.2 MODULE | DEFECTS CA | LSSIFICA | ΓΙΟΝ | | | |
| NO. | ITEM | | | CRITERIA | | | |
| 1. | DISPLAY ON INSPECTION | (1)INCORRECT (2)MISSING SE (3)DIM SEGME (4)OPERATING | GMENT NT | BEYOND SPEC | | | |
| 2. | OVERALL DIMENSIONS | (1)OVERALL D | IMENSION I | BEYOND SPEC | | | |
| | | (1) SPOTS | | | | | |
| | | AVERAGE I | : D | NUMBER OF PIE PERMITTED | | MINIMI SPAC | l I |
| | | D≤0 | .2 | IGNORE | | | |
| | | 0.2 <d< td=""><td>≤0.4</td><td>5</td><td></td><td>10 mm</td><td>n</td></d<> | ≤0.4 | 5 | | 10 mm | n |
| | | 0.4< | | 0 | | | |
| 3. | BLACK SPOTS, FOREIGN MATTER, AND WHITE SPOTS (INCLUDING LIGHT | NOTE : THAT WI | HEN THERE | S IS TO BE SET WIT ARE 2 PIECES OR MISIDERED AS CONC | ИORE, Т | ГНЕҮ | |
| ٥. | LEAKAGE DUE TO | \ <u> </u> | ` | FULLY POWERED- | , | | |
| | POLARIZING PLATES | AVERAG | | BER OF PIECES Not | e : Diar | meter D=(a | ı+b)/2 |
| | PINHOLES, ETC.) | DIAMETER (n | 1m): D P | ERMITTED IGNORE | | | \wedge |
| | | D≤0.3 | 5 | 5 | | |) b |
| | | | | | | | \perp |
| | | NUMBER OF TOTAL PIECES IS TO BE SET WITHIN 5 PIECES. | | | | | |
| | | NOTE: | | | | | |
| | | | | ARE 2 PIECES OR N | | | |
| | DI ACIVI DIE | | | ISIDERED AS CONC | | ATED. | |
| 4. | BLACK LINE WHITE LINE | VIEWING A | | E LINE ARE WITHI OT ALLOW | NIHE | | |
| , | NON-DISPLAY | VIEWING III | XL11. 11 15 1V | OT MELOW. | | | |
| 1 | | ` ' | | CK LINE , WHITE LI | | E WITHIN | THE |
| | | | | H:Wmm, LENGH:L | | Magibir | 1110 |
| 5 | BLACK LINE WHITE | LENGT L ≤ | | $\frac{\text{WIDTH : W}}{\text{W} \le 0.1}$ | PERI | MISSIBLE IGNORE | NO. |
| , 3 | LINE ON-DISPLAY | 0.5 < L | | $0.1 < W \le 0.3$ | | 4 | |
| | | 2.5 | | $0.3 \le W$ | | NONE | |
| | | | | | | | |
| | SCRATCHES AND | | | OVE NO.3 AND 4 TO | | | |
| 6. | DENT ON GLASS POLARIZER | DETERMINE OR GLASS | SCRATCHE | S AND DENT ON PO | ULARIZ | ZER | |
| | FOLARIZER | | udgment Criter | ria | | | _ |
| | | | | rk Dot Total | | te: A/B | |
| | | A | 3 | 3 4 5 5 | → | Definition | → → |
| | | В | 5 | | 1/5 | 3/5 B Area | 1/5 |
| 7. | DOT DEFECT | ` ' | | if defect area>0.5dot | *** | D Then | |
| . | ON DISPLAY | (2) It is ignored if | | .5dot efined as Bright Dot | 3/5 | A Area | |
| . | | if it can be observ | | | | | |
| . | | (4)The distance b | etween 2 dot | defect≥5mm | 1/5 | | |
| , L | | (5)Not Allowed J | oint point def | ect | | | İ |

| NO. | ITEM | CRITERIA | | |
|-----|--|--|--|--|
| 8 | LINE DEFECT ON DISPLAY | OBVIOUS VERTICAL OR HORIZONTAL LINE DEFECT IS NOT ALLOW | | |
| 9 | MURA ON DISPLAY | IT'S OK IF MURA IS SLIGHT VISIBLE THROUNG 6% ND FILTER | | |
| 10 | CF FAIL/SPOT ON DISPLAY | $(1) THE \ FOLLOWING \ CF \ FAIL \ , \ SPOT \ ARE \ WITHIN \ THE \ VIEWING \ AREA $ $\begin{array}{ c c c c c c c c c c c c c c c c c c c$ | | |
| 11 | UNEVEN COLOR SPREAD , COLORATION | (1)TO BE DETERMINED BASED UPON THE STANDARD SAMPLE. | | |
| 12 | BEZEL APPEARANCE | (1)BEZEL MAY NOT HAVE RUST, E DEFORMED OR HAVE FINGER PRINTS STAINS OF OTHER CONTAMINATION. (2)BEZEL MUST COMPLY WITH JOB SPECIFICATIONS. | | |
| 13 | SOLDERING | (1)NO SOLDERING FOUND ON THE SPECIFIED PLACE (2)INSUFFICENT SOLDER (a)LSI, IC A POOR WETTING OF SOLDER IS BETWEEN LOWER BEND OR "HEEL" OF LEAD AND PAD SOLDER FILLET (b)CHIP COMPONENT . SOLDER IS LESS THAN 50% OF SIDES AND FRONT FACE WETTING SOLDER FILLET | | |

| NO. | ITEM | CRITERIA |
|--------|---------|--|
| | | SOLDER WETS 3 SIDES OF TERMINAL, BUT LESS THAN 25% OF SIDES AND FRONT SURFACE AREA ARE COVERED |
| | | SOLDER |
| 13. SO | LDERING | (3)PARTS ALIGMENT (a)LSI, IC LEAD WIDTH IS MORE THAN 50% BEYOND PAD OUTLINE |
| | | |
| | | (b)CHIP COMPONENT COMPONENT IS OFF CENTER, AND MORE THAN 50% OF THE LEADS IS OFF THE PAD OUTLINE |
| | | |

| NO. | ITEM | CRITERIA | |
|-----|-----------------------|--|--|
| 13 | SOLDERING | (4)NO UNMELTED SOLDER PASTE MAY BE PRESENT ON THE PCB. | |
| | | (5)NO COLD SOLDER JOINTS, MISSING SOLDER | |
| 13. | | CONNECTIONS, OXIDATION OR ICICLE. | |
| | | (6)NO RESIDUE OR SOLDER BALLS ON PCB. | |
| | | (7)NO SHORT CIRCUITS IN COMPONENTS ON PCB. | |
| | | (1)NO LIGHT | |
| | | (2)FLICKERING AND OTHER ABNORMAL ILLUMINATION | |
| 14. | BACKLIGHT | (3)SPOTS OR SCRATCHES THAT APPEAR WHEN LIT | |
| | | MUST BE JUDGED USING LCD SPOT, LINES AND | |
| | | CONTAMINATION STANDARDS. | |
| | | (4)BACKLIGHT DOESN'T LIGHT OR COLOR IS WRONG. | |
| | | (1)NO OXIDATION, CONTAMINATION, URVES OR, BENDS ON INTERFACE PIN (OLB) OF TCP. | |
| | | (2)NO CRACKS ON INTERFACE PIN (OLB) OF TCP. | |
| | | (3)NO CONTAMINATION, SOLDER RESIDUE OR SOLDER | |
| | | BALLS ON PRODUCT. | |
| | | (4)THE IC ON THE TCP MAY NOT BE DAMAGED, CIRCUITS. | |
| | GENERAL APPEARANCE | (5)THE UPPERMOST EDGE OF THE PROTECTIVE STRIP ON | |
| | | THE INTERFACE PIN MUST BE PRESENT OR LOOK AS | |
| | | IF IT CAUSE THE INTERFACE PIN TO SEVER. | |
| | | (6)THE RESIDUAL ROSIN OR TIN OIL OF SOLDERING | |
| | | (COMPONENT OR CHIP COMPONENT) IS NOT BURNED | |
| 15. | | INTO BROWN OR BLACK COLOR. | |
| | | (7)SEALANT ON TOP OF THE ITO CIRCUIT HAS NOT | |
| | | HARDENED. | |
| | | (8)PIN TYPE MUST MATCH TYPE IN SPECIFICATION SHEET. | |
| | | (9)LCD PIN LOOSE OR MISSING PINS. | |
| | | (10)PRODUCT PACKAGING MUST THE SAME AS | |
| | | SPECIFIED ON PACKAGING SPECIFICATION SHEET. | |
| | | (11)PRODUCT DIMENSION AND STRUCTURE MUST | |
| | | CONFORM TO PRODUCT SPECIFICATION SHEET. | |
| | | (12)THE APPEARANCE OF HEAT SEAL SHOULD NOT | |
| | | ADMIT ANY DIRT AND BREAK. | |

| NO. | ITEM | CRITERIA | | | |
|-----|---------------|--|--|---------------------------------|-------------|
| | | THE LCD WITH EXTENSIVE CRACK IS NOT ACCEPTABLE | | | |
| | | General glass chip: | a | b | c |
| | | a b | $\leq t/2$ | < VIEWING AREA | $\leq 1/8X$ |
| | | C C | $t/2 > , \le 2t$ | ≤ W/2 | ≤ 1/8X |
| | | | *W=DISTAN | NCE BETWEEN S | SEALAN |
| | 1 | | AREA A | AND LCD PANEI | EDGE. |
| | | | | IDE LENGTH | |
| | | W C | t = GLASS | S THICKNESS | |
| | | a | | | |
| | | | | | |
| | | b | | | |
| | | c L | | | |
| | | | | | |
| | | \b\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\ | | | |
| | | a | | | |
| | | Corner part: | a | b | c |
| | | \ b | ≤ t/2 | < VIEWING AREA | ≤ 1/8X |
| | | $c \downarrow$ | $> t/2$, $\leq 2t$ | ≤ W/2 | $\leq 1/8X$ |
| | | | | NCE BETWEEN S | |
| 16. | CRACKED GLASS | a | | AND LCD PANEI | L EDGE. |
| | | | | IDE LENGTH | |
| | | CHID ON ELECTRODE DAD | | S THICKNESS | |
| | | CHIP ON ELECTRODE PAD | a | b ≤ 0.5mm | C 1/0V |
| | | b c | ≤ t * V_I CD CI | l l | ≤ 1/8X |
| | | | * X=LCD SIDE WIDTH t =GLASS THICKNESS | | |
| | | | a | b | С |
| | | | a ≤ t | ≤ 1/8X | ≤ L |
| | | | *X=LCD SII | | _ _ |
| | | | | THICKNESS | |
| | | | | ODE PAD LENG | TH |
| | | | | CHIPPING THE IT | |
| | | | TERMINA | L, OVER 2/3 OF TI | HE ITO |
| | | | | MAIN AND BE, | |
| | | - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 - 1 | | D ACCORDING TO | O |
| | | | | DE TERMINAL | |
| | | | SPECIFICA ②IF THE PRO | ATIONS ODUCT WILL BE I | НБАТ |
| | | | | | |
| | | | I SEALED B | T THE CUSTOME | K. IHP. |
| | | | | Y THE CUSTOME NT MARK MUST I | |

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12.4 RELIABILITY TEST

12.4.1 STANDARD SPECIFICATIONS FOR RELIABILITY OF LCD MODULE

| NO | ITEM | DESCRIPTION |
|----|--------------------------------------|---|
| 1 | High temperature operation | The sample should be allowed to stand at +70°C for 240 hrs |
| 2 | Low temperature operation | The sample should be allowed to stand at -20°C for 240 hrs |
| 3 | High temperature storage | The sample should be allowed to stand at +80°c for 240 hrs |
| 4 | Low temperature storage | The sample should be allowed to stand at –30°C for 240 hrs |
| 5 | High temp / humidity test | The sample should be allowed to stand at 60°C, 90% RH 240 hrs |
| 6 | Thermal shock (not operated) | The sample should be allowed to stand the following 200 cycles of operation: -25°c for 30 minutes ~ +70°c for 30 minutes |
| 7 | ESD (Electrostatic Discharge) | AIR DISCHARGE ± 4KV CONTACT DISCHARGE ± 2KV |

NOTE (1): THE TEST SAMPLES HAVE RECOVERY TIME FOR 2 HOURS AT ROOM TEMPERATURE BEFORE THE FUNCTION CHECK. IN THE STANDARD CONDITIONS, THERE IS NO DISPLAY FUNCTION NG ISSUE

OCCURRED.

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12.5 TESTING CONDITIONS AND INSPECTION CRITERIA

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in table 12.5, standard specifications for reliability have been executed in order to ensure stability.

| NO | ITEM | TEST MODEL | INSPECTION CRITERIA |
|----|-------------|------------------------|--|
| 1 | Current | Pater In Spacification | The current consumption should |
| | consumption | | conform to the product specification. |
| | | | After the tests have been executed, |
| 2 | Contrast | - | the contrast must be larger than half |
| | | | of its initial value prior to the tests. |
| 3 | Appearance | Visual inspection | Defect free |

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12.6 OPERATION

- 12.6.1 Do not connect or disconnect modules to or from the main system while power is being supplied.
- 12.6.2 Use the module within specified temperature; lower temperature causes the retardation of blinking speed of the display; higher temperature makes overall display discolor. When the temperature returns to normality, the display will operate normally.
- 12.6.3 Adjust the LC driving voltage to obtain the optimum contrast.
- 12.6.4 Power On Sequence input signals should not be supplied to LCD module before power supply voltage is applied and reaches the specified value . If above sequence is not followed, CMOS LSIs of LCD modules may be damaged due to latch up problem.

12.7 NOTICE

- 12.7.1 Use a grounded soldering iron when soldering connector I/O terminals . For soldering or repairing , take precaution against the temperature of the soldering iron and the soldering time to prevent peeling off the through-hole-pad .
- 12.7.2 Do not disassemble . EDT shall not be held responsible if the module is disassembled and upon the reassembly the module failed .
- 12.7.3 Do not charge static electricity, as the circuit of this module contains CMOS LSIs. A workman's body should always be static-protected by use of an ESD STRAP. Working clothes for such personnel should be of static-protected material.
- 12.7.4 Always ground the electrically-powered driver before using it to install the LCD module. While cleaning the work station by vacuum cleaner, do not bring the sucking mouth near the module; static electricity of the electrically-powered driver or the vacuum cleaner may destroy the module.
- 12.7.5 Don't give external shock.
- 12.7.6 Don't apply excessive force on the surface.
- 12.7.7 Liquid in LCD is hazardous substance. Must not lick and swallow. When the liquid is attach to your, skin, cloth etc. Wash it out thoroughly and immediately.
- 12.7.8 Don't operate it above the absolute maximum rating.
- 12.7.9 Storage in a clean environment, free from dust, active gas, and solvent.
- 12.7.10 Store without any physical load.
- 12.7.11 Rewiring: no more than 3 times.